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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/706,076	11/03/2000	Richard A. Willems	PD99-2788	6153	
22879 7	590 04/16/2004		EXAMI	EXAMINER	
HEWLETT PACKARD COMPANY P O BOX 272400, 3404 E. HARMONY ROAD INTELLECTUAL PROPERTY ADMINISTRATION FORT COLLINS, CO 80527-2400			STEELMAN	STEELMAN, MARY J	
			ART UNIT	PAPER NUMBER	
			2122	\bigcap	
		•	DATE MAILED: 04/16/2004	9	

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)	\		
	09/706,076	WILLEMS, RICHAF	RD A.		
Office Action Summary	Examiner	Art Unit			
	Mary J. Steelman	2122			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply					
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period w Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply within the statutory minimum of thirty (3 vill apply and will expire SIX (6) MONTHS cause the application to become ABAN	y be timely filed 10) days will be considered timely. S from the mailing date of this cor DONED (35 U.S.C. § 133).			
Status					
1) Responsive to communication(s) filed on 13 Fe					
<i>;</i>	action is non-final.				
3) Since this application is in condition for allowar	•	• •	merits is		
closed in accordance with the practice under E	х рапе Quayle, 1935 С.D. 1	1, 453 O.G. 213.			
Disposition of Claims					
4) ☐ Claim(s) 1-17 is/are pending in the application. 4a) Of the above claim(s) is/are withdraw 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1-17- is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or	vn from consideration.				
Application Papers					
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) acce Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Ex	epted or b) objected to by drawing(s) be held in abeyance ion is required if the drawing(s)	. See 37 CFR 1.85(a). is objected to. See 37 CFI	• •		
Priority under 35 U.S.C. § 119					
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the prior application from the International Bureau * See the attached detailed Office action for a list of	s have been received. s have been received in App ity documents have been re- i (PCT Rule 17.2(a)).	lication No ceived in this National S	Stage		
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	Paper No(s)/M	nmary (PTO-413) fail Date mal Patent Application (PTO-	-152)		

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DETAILED ACTION

1. This action is in response to RCE filed -2/13/2004.

Double Patenting

2. In response to the Terminal Disclaimer filed 02/13/2004, the prior Double Patenting rejection is hereby withdrawn.

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claims 1-17 are rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent 6,158,045 to You, in view of "Compilers Principles, Techniques, and Tools" by Alfred V. Aho, Ravi Sethi and Jeffrey D. Ullman, pages 432-433, 439, and 703-711, and further in view of U.S. Patent Application 2003/0200397 to McAllister et al.
- 5. You disclosed symbolic debugging services utilizing a client debugger object, a connection object and a server debugger object. (Abstract, lines 9-11) "An addressing abstraction is utilized to facilitate the use of target memory addresses..." The client debugger object transmits debug requests to a target server debugger object. "Figure 4, the collection represents a possible layout for the memory locations in which pointers to the objects of type Aobject are stored...(col. 11, lines 42)." Although You discloses that memory is accessed, he failed to disclose that memory can be structured as linked lists, and linked lists of linked lists (binary tree). However, Aho disclosed the use of linked lists in memory (page 432-433 and

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information retrieval from the nodes (page 439). Official Notice is given that binary trees are special forms of linked lists whereby the first linked list can contain the head of a second linked list. Traversal to access data contained in the nodes of data structures and data accessing are well known.

Per claims 1 and 11:

-following a plurality of memory element descriptors of a machine readable record list to locate data in the memory of the computer system, where each memory descriptor is descriptive of data to be retrieved from memory of the computer system; (You: Fig. 11 (client requests) and col. 6, lines 42-52, "Interactive debuggers may have textual or graphical use interfaces.")

-gathering data specified by the plurality of memory element descriptors; formatting the data into a buffer. (You: Col. 6, lines 20-22, "A program built on top of a primitive debugger can exploit the capabilities of the debugger to gather dynamic information about the program.")

Neither You nor Aho discuss "gathering data ...while maintaining data coherency", a newly added limitations to the independent claims. However McAllister disclosed memory transaction coherency through the use of a memory controller agent (Page 3, [0022]). McAllister disclosed, at page 3, [0023], "The agent is responsible for ensuring coherency and fulfilling memory transactions for a single memory line, thereby simplifying the design of the agent."

Therefore, it would have been obvious, to one of ordinary skill in the art, at the time of the invention, to include information regarding binary trees as useful data structures in memory for storage and retrieval of information because linked lists are useful for creating arrays of unknown size, using non contiguous regions of memory, and retrieving data does not remove or destroy the item and furthermore to include details on data coherency when retrieving data from

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memory because fresh data, not stale, "dirty" data is necessary when attempting to debug memory.

Per claim 2: (You: Fig. 4 and col. 11, line 32, col. 13, lines 12-13 and 22-23, "...a possible layout for the memory locations...")

Per claim 3: (You: Fig. 5, #511 and col. 55, lines 55-61, "The scalar types long, short, char, signed char, unsigned l9ng...are read and written transparently...Arbitrary blocks of memory and char strings are transported correctly.")

Per claim 4: (You: Col. 14, lines 34-36, "Retrieval from the collection has a uniform, polymorphic interface. This is achieved by calling the Get function which takes an object of the parameterized type.")

Per claims 5 and 12: (Aho: Page 446-449, "A Fortran compiler can create a number of data areas, i.e., blocks of storage in which the values of objects can be stored..." For each data area the compiler creates a memory map...")

Per claim 6:

- -constructing a record list, the record list comprising at least a first list element descriptor descriptive of data to be retrieved from a first linked list;
- -following a list head locator of the list element descriptor to a head of the first linked list;
- -following links of the head of the first linked list to a first node of the linked list;
- -interpreting at least one tag of the first list element descriptor to locate data of the node;
- -extracting data from the node. (You: Col. 6, lines 20-30, "...exploit the capabilities of the debugger to gather dynamic information about the program...")

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Per claims 7 and 12: (Official Notice that binary tree: node of first linked list contains a head of the second linked list are well known.)

Per claims 8 and 9: (You: Col. 8, lines 45-65, "...Multiple threads can execute concurrently or through simulated concurrency..." "...multiple processes and threads can be executing under the control of a single debugger." "Multithreaded programs can be debugged so that some threads may be stopped and others remain executing while the debugger is also executing." Per claim 10: (Aho: Page 439, "A pointer front points to the most recently created entry I the list." And "The implementation of lookup is done by scanning the list starting at the entry pointed to by front and following links until the desired name is found...")

Per claim 13:

- -a collection driver for execution on the target machine; (You: col. 6, lines 38-43, "Interactive program debuggers...")
- -a user interface capable of coupling to the collection driver; (You: Col. 6, line 43, "...graphical user interfaces."
- -a symbol resolution system capable of coupling to the user interface; (You, col. 6, lines 45-52, "...view his program through the representation of program symbols. The program symbols are typically the names of subroutines, classes, types, variables, and other program constructs..." -wherein the user interface comprises computer readable code for constructing an input record list containing records describing data to be captured, at least some records of the input record list containing information derived from symbols resolved by the symbol resolution system, and transmitting the input record list to the collection driver; (You: Col. 6, lines 20-30.)

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You, figure 11, preparing a client request.)

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-wherein the collection driver further comprises code for interpreting the input record list and collecting operating system data into a capture buffer specified by the input record list, and transmitting the capture buffer to the user interface. (You: Col. 6, lines 1-52.)

Per claim 14: (You: Col. 6, lines 20-22, "A program built on top of a primitive debugger can exploit the capabilities of the debugger to gather dynamic information about the program...")

Per claim 15: (Official Notice: Retrieval of data from nodes of a tree are well known. Also see

Per claim 16: (You: Col. 55, lines 61-63, "The PDS streams provide a simple abstraction that allows objects to be moved from the writer of the stream to the reader of the stream." And col. 55, lines 55-60, "The scalar types ...are read and written transparently..." And col. 55, lines 62-63, "...a buffered streaming class is provided..." Also see You, figure 5.)

Per claim 17: (You: Abstract, line 9, "Clients can process locally and remotely.")

Response to Arguments

6. Applicant has argued, in substance, the following:

(A) As Applicant has noted on page5, 2nd and 3rd paragraphs of RCE, the cited prior art "fails to point to any teaching, suggestion, or motivation in You or McAllister for combining the subject matter disclosed..." "You and McAllister are related to different fields, address different problems, and provide unrelated solutions.

Examiner's Response:

Claim limitations to "locate data in the memory", by following "a plurality of memory element descriptors", where "each memory descriptor is descriptive of data to be retrieved from memory..." can be correctly mapped to a debug operation that retrieves memory values, as

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disclosed by You, as well as a "memory controller that provides coherence" as disclosed by McAllister. The common feature of the two references is that data is gathered from memory. Providing coherency, of course, ensures the reliability of the gathered data. See McAllister, [0061], "...if the contents (of a cache memory) have been altered, then the contents must be written back to main memory unit at some point to keep the main memory coherent..." Maintaining memory coherency is well known in the art and necessary for proper functioning of a software program.

In response to applicant's argument that the examiner's conclusion of obviousness is based upon improper hindsight reasoning, it must be recognized that any judgment on obviousness is in a sense necessarily a reconstruction based upon hindsight reasoning. But so long as it takes into account only knowledge which was within the level of ordinary skill at the time the claimed invention was made, and does not include knowledge gleaned only from the applicant's disclosure, such a reconstruction is proper. See *In re McLaughlin*, 443 F.2d 1392, 170 USPQ 209 (CCPA 1971).

Examiner maintains the rejections of claims 1-17.

Conclusion

- 7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
- 8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mary Steelman, whose telephone number is (703) 305-4564. The examiner can normally be reached Monday through Thursday, from 7:00 A.M. to 5:30 P.M. If

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attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tuan Dam can be reached on (703) 305-4552.

The fax phone number is (703) 872-9306 for regular communications and for After Final communications. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.

Mary Steelman Many Hulm

04/08/2004

Hvængen ar Torrej Rejnegen Ba ANTONY NGUYEN-BA PRIMARY EXAMINER